

Description

The XPX4132FD uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

Application

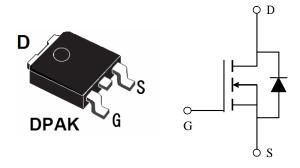
Battery protection

Load switch

Uninterruptible power supply



 $V_{DS} = 30V, I_{D} = 120A$ $R_{DS}(ON) = 2.4 m\Omega$ (typ) @ $V_{GS} = 10V$ $R_{DS}(ON) = 3.5 m\Omega$ (typ) @ $V_{GS} = 4.5V$



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
XPX4132FD	TO-252-3	XPX4132FD XXX YYYY	2500

Absolute Maximum Ratings (TC=25°C unless otherwise specified)

Symbol	Parameter	Rating	Units	
VDS	Drain-Source Voltage	30	V	
Vgs	Gate-Source Voltage	±20	V	
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ^{1,6}	120	А	
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ^{1,6}	98	А	
Ідм	Pulsed Drain Current ²	270	А	
EAS	Single Pulse Avalanche Energy ³	215	mJ	
las	Avalanche Current	70.2	А	
P _D @T _C =25°C	Total Power Dissipation ⁴	89.3	W	
Тѕтс	Storage Temperature Range	-55 to 175	°C	
TJ	Operating Junction Temperature Range	-55 to 175	°C	
Reja	Thermal Resistance Junction-Ambient ¹	62	°C/W	
Rejc	Thermal Resistance Junction-Case ¹	1.4	°C/W	



Symbol	Parameter Conditions		Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage V _{GS} =0V , I _D =250uA		30			V
△BV _{DSS} /△T _J	BV _{DSS} Temperature Coefficient Reference to 25°C , I _D =1mA			0.022		V/°C
		V _{GS} =10V , I _D =30A		2.4	3	
Rds(on)	Static Drain-Source On-Resistance ²	V _{GS} =4.5V , I _D =15A		3.5	4	mΩ
V _{GS(th)}	Gate Threshold Voltage		1		2.5	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	V _{GS} =V _{DS} , I _D =250uA		-6.1		mV/°C
Ipss	Desire Courses Lockers Courses	V _{DS} =24V , V _{GS} =0V , T _J =25°C			2	
IDSS	Drain-Source Leakage Current	V _{DS} =24V , V _{GS} =0V , T _J =55°C			10	uA
Igss	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA
gfs	Forward Transconductance	ard Transconductance V _{DS} =5V , I _D =30A		60		S
Rg	Gate Resistance	Gate Resistance V _{DS} =0V , V _{GS} =0V , f=1MHz		0.9		
Qg	Total Gate Charge (4.5V)			56.9		
Qgs	Gate-Source Charge	V _{DS} =15V , V _{GS} =10V , I _D =15A		13.8		nC
Qgd	Gate-Drain Charge			23.5		
T _{d(on)}	Turn-On Delay Time			20.1		
Tr	Rise Time	V _{DD} =15V , V _{GS} =10V ,		6.3		
T _{d(off)}	Turn-Off Delay Time	R _G =3.3 ,		124.6		ns
T _f	Fall Time	I _D =1A		15.8		
Ciss	Input Capacitance			3413		
Coss	Output Capacitance V _{DS} =15V , V _{GS} =0V , f=1MHz			725		pF
Crss	Reverse Transfer Capacitance			538		· I
ls	Continuous Source Current ^{1,5}				155	Α
lsм	Pulsed Source Current ^{2,5}	−V _G =V _D =0V , Force Current			310	Α
Vsp	Diode Forward Voltage ²	V _{GS} =0V , I _S =A , T _J =25°C			1.2	V

Note:

- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2 .The data tested by pulsed , pulse width \leqq 300us , duty cycle \leqq 2%
- 3. The EAS data shows Max. rating . The test condition is V_{DD} =25 V, V_{GS} =10V, L=0.1 mH, I_{AS} =70.2 A
- 4.The power dissipation is limited by 150°c junction temperature
- 5 .The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation. 6.Package limitation current is 85A.



Typical Characteristics

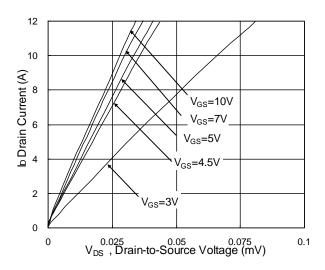


Fig.1 Typical Output Characteristics

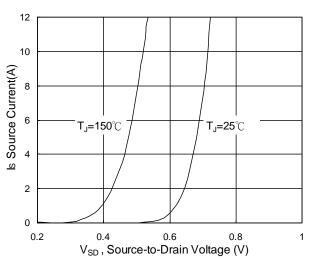


Fig.3 Forward Characteristics of Reverse

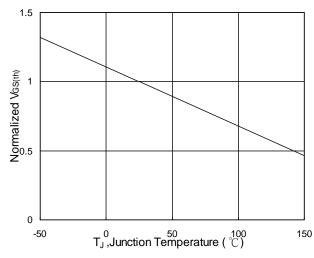


Fig.5 Normalized V_{GS(th)} v.s T_J

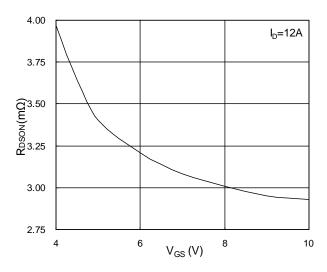


Fig.2 On-Resistance v.s Gate-Source

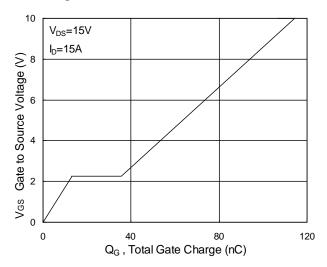


Fig.4 Gate-Charge Characteristics

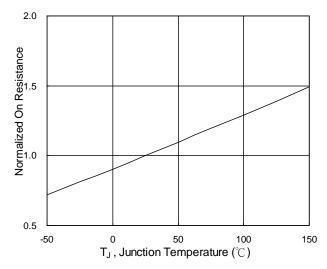
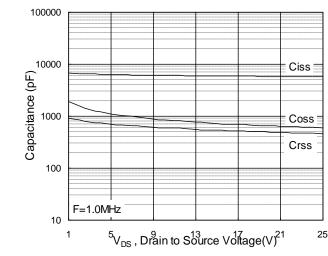


Fig.6 Normalized R_{DSON} v.s T_J





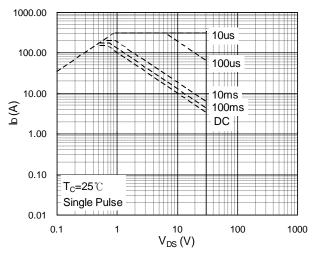


Fig.7 Capacitance

Fig.8 Safe Operating Area

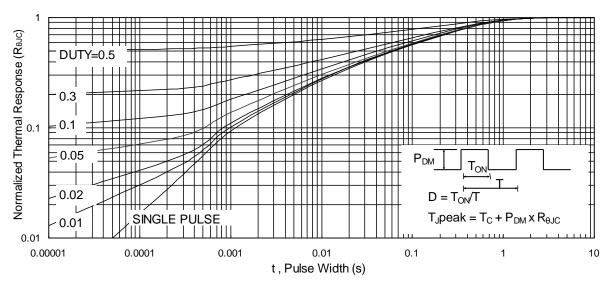


Fig.9 Normalized Maximum Transient Thermal Impedance

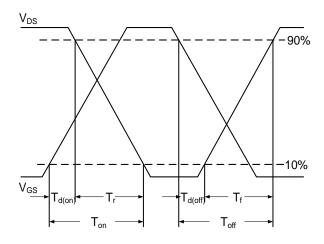


Fig.10 Switching Time Waveform

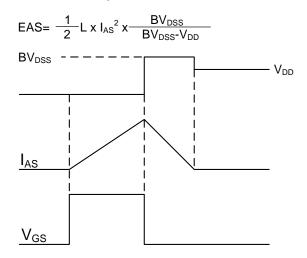
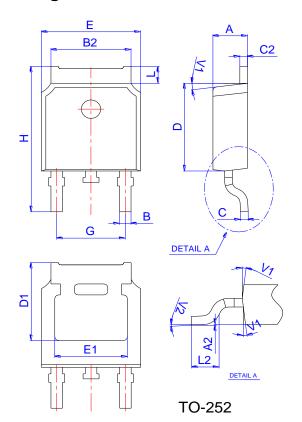


Fig.11 Unclamped Inductive Waveform

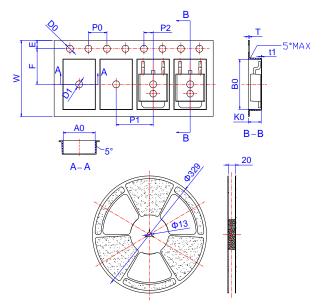


Package Mechanical Data



	Dimensions					
Ref.	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
В	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
С	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF		0.209REF			
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
Н	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°

Reel Spectification-TO-252



	Dimensions					
Ref.	Millimeters		Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.
W	15.90	16.00	16.10	0.626	0.630	0.634
Е	1.65	1.75	1.85	0.065	0.069	0.073
F	7.40	7.50	7.60	0.291	0.295	0.299
D0	1.40	1.50	1.60	0.055	0.059	0.063
D1	1.40	1.50	1.60	0.055	0.059	0.063
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.90	2.00	2.10	0.075	0.079	0.083
A0	6.85	6.90	7.00	0.270	0.271	0.276
В0	10.45	10.50	10.60	0.411	0.413	0.417
K0	2.68	2.78	2.88	0.105	0.109	0.113
Т	0.24		0.27	0.009		0.011
t1	0.10			0.004		
10P0	39.80	40.00	40.20	1.567	1.575	1.583



Flow (wave) soldering (solder dipping)

Product	Peak Temperature	Dipping Time		
Pb device	245℃±5℃	5sec±1sec		
Pb-Free device	260℃+0/-5℃	5sec±1sec		



This integrated circuit can be damaged by ESD UniverChip Corporation recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedure can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

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