

Description

The XPX8726 uses advanced trench technology and design to provide excellent $R_{\rm DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

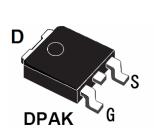
Application

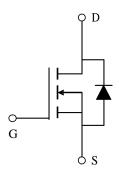
- DC/DC converters
- Synchronous Rectifier

VDS =30V,ID =90A

RDS(ON) =4.2m Ω (typ) @ VGS=10V

RDS(ON) =5.5m Ω (typ) @ VGS=4.5V





Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
XPX8726	XPX8726	TO-252-2L	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	30	V	
Gate-Source Voltage	V _{GS}	±20	V	
Drain Current-Continuous	I _D	90	Α	
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	62	Α	
Pulsed Drain Current	I _{DM}	200	А	
Maximum Power Dissipation	D	103	W	
Derating factor	P _D	0.6	W/°C	
Single pulse avalanche energy (Note 5)	E _{AS}	370	mJ	
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}$	



Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	R _{eJC}	1.51	°C/W
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Electrical Characteristics (T_C=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	30	38	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V,V _{GS} =0V	-	-	1	μΑ
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)			•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1	1.6	2.3	V
Drain-Source On-State Resistance		V _{GS} =10V, I _D =20A	-	4.2		
Diani-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =20A		5.9	7.5	mΩ
Forward Transconductance	G FS	V _{DS} =5V,I _D =20A	20	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ 45\/\\ 0\/	-	1356	-	PF
Output Capacitance	C _{oss}	V_{DS} =15V, V_{GS} =0V, F=1.0MHz	-	55	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVIFIZ	-	45	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t _{d(on)}		-	8	-	nS
Turn-on Rise Time	t _r	V_{DD} =15 V , I_D =20 A	-	9	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{GEN} =3 Ω	-	32	-	nS
Turn-Off Fall Time	t _f		-	6	-	nS
Total Gate Charge	Qg	\/ -15\/ -45^	-	25	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =15V, I_{D} =45A, V_{GS} =10V	-	5	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	3	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =20A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	90	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = 20A	-	29	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	32	-	nC

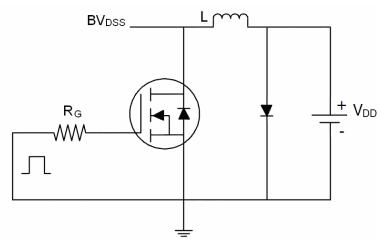
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- **3.** Pulse Test: Pulse Width ≤ 300μ s, Duty Cycle ≤ 2%.
- **4.** Guaranteed by design, not subject to production
- **5.** EAS condition: Tj=25 $^{\circ}$ C, V_{DD}=15V,V_G=10V,L=0.5mH, Rg=25 Ω ;

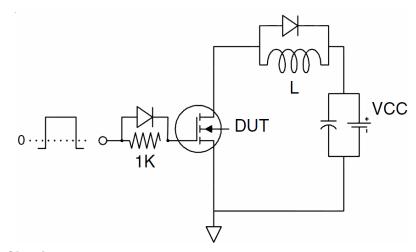


Test Circuit

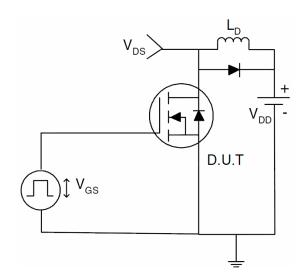
1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics (Curves)

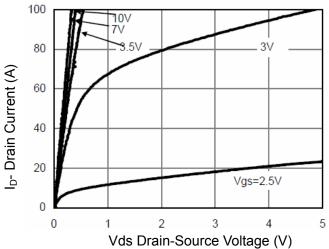


Figure 1 Output Characteristics

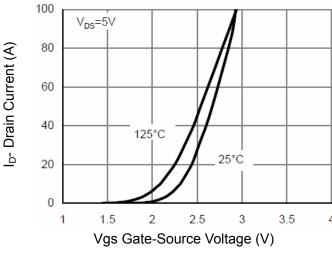


Figure 2 Transfer Characteristics

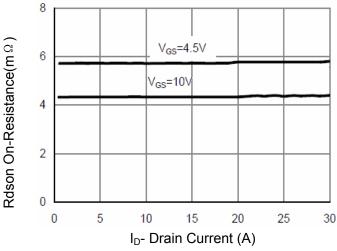


Figure 3 Rdson- Drain Current

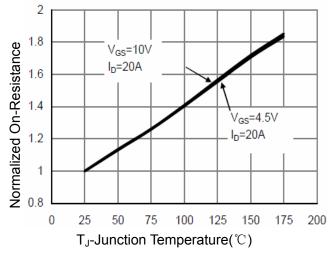
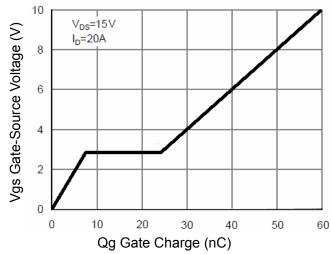


Figure 4 Rdson-Junction Temperature



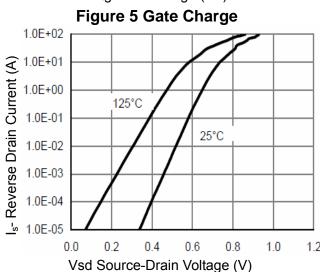


Figure 6 Source- Drain Diode Forward



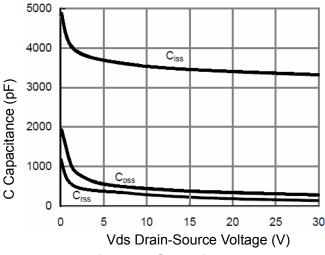


Figure 7 Capacitance vs Vds

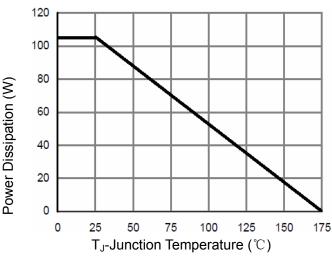


Figure 9 Power De-rating

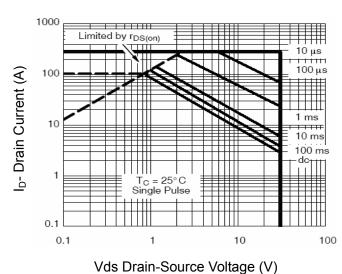


Figure 8 Safe Operation Area

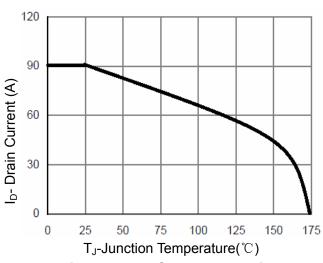


Figure 10 ID Current Derating

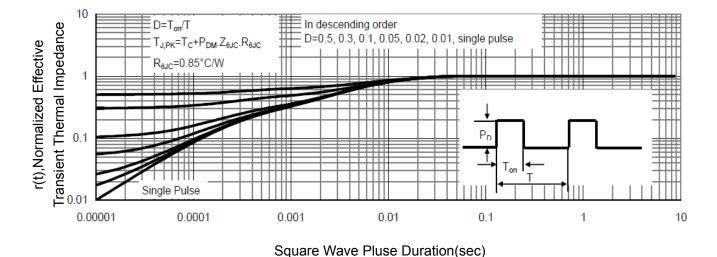
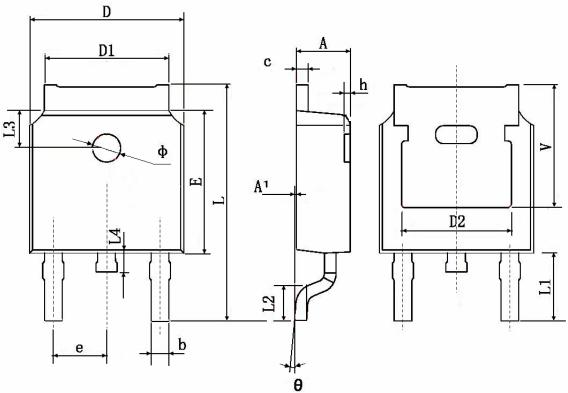


Figure 11 Normalized Maximum Transient Thermal Impedance



TO-252-2L Package Information



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Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	0.483	TYP.	0.190 TYP.		
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900 TYP.		0.114 TYP.		
L2	1.400	1.700	0.055	0.067	
L3	1.600 TYP.		0.063 TYP.		
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350 TYP.		0.211	TYP.	



Flow (wave) soldering (solder dipping)

Product	Peak Temperature	Dipping Time
Pb device	245℃±5℃	5sec±1sec
Pb-Free device	260℃+0/-5℃	5sec±1sec



This integrated circuit can be damaged by ESD UniverChip Corporation recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedure can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

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